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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/789,496	02/26/2004	David C. Nemir	70004-9601-CIP2	9093
5179	7590	10/18/2006	EXAMINER	
PEACOCK MYERS, P.C.			A, MINH D	
201 THIRD STREET, N.W.				
SUITE 1340			ART UNIT	PAPER NUMBER
ALBUQUERQUE, NM 87102			2821	

DATE MAILED: 10/18/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)
	10/789,496	NEMIR ET AL.
	Examiner Minh D. A	Art Unit 2821

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 7/24/06.
 2a) This action is FINAL. 2b) This action is non-final.
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 1,27-40, 42-55 is/are pending in the application.
 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
 5) Claim(s) _____ is/are allowed.
 6) Claim(s) 1 and 27-40, 42-55 is/are rejected.
 7) Claim(s) _____ is/are objected to.
 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.
 10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413)
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Date. _____
3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____	5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)
	6) <input type="checkbox"/> Other: _____

DETAILED ACTION

1. Applicant 's communication filed on 7/29/06 has been carefully considered by the examiner. The arguments advanced therein are persuasive with respect to the rejection of record, and those rejection are accordingly withdraw. In view of a further consideration, however, a new rejection is set forth below. This action is not made final.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 1, 27-36, 38-40, 42-49, 51 are rejected under 35 U.S.C. 102(b) as being anticipated by DePauli (US 4, 963, 793).

Regarding claim 1, DePauli discloses a programmable control unit(20) for outlet adapter and an apparatus being entirely resident within an appliance plug (see figure 1), the plug (5) comprising power delivery conductors and the apparatus comprising a programmable controller(20) for programming exclusively through a plurality of the power delivery conductors (100a). See figures 1, col.4, lines 1-67 to col.5, lines 1-67.

Regarding claim 27, DePauli discloses the plurality numbers conductors no more than four. See figure 1.

Regarding claim 28, DePauli discloses wherein two of conductors no more than four are electrically shorted together such that said plurality of power delivery

conductors consists of not more than three electrically unique power delivery conductors. See figure 1.

Regarding claim 29, DePauli discloses the control processor (20) for applying two of said not more than three electrically unique power delivery conductors. See figures 1 .

Regarding claim 30, DePauli Fowler discloses wherein said programming signal comprises time pulse. Figure 1.

Regarding claims 31-32, DePauli discloses wherein a mixture of direct and alternating current signals is applied to two of said three electrically unique power delivery conductors. See figures 1 .

Regarding claim 33, DePauli discloses the mixture of direct and alternating current signals places said programmable controller (20) into a programming mode. See figure 1 .

Regarding claims 34-35, DePauli discloses wherein at least one of said signals comprises a high frequency signal or wherein said programmable controller is electronically configured to implement a set of control actions.

Regarding claim 36, DePauli discloses the programmable controller (20)comprises a microcontroller. See figure 1, col.4, lines 26-67.

Regarding claim 38, DePauli discloses the programmable controller (20) is programmed via electronic signals from a programmer. See figure 1.

Regarding claim 39, DePauli discloses the controller is programmable after said apparatus is assembled and the controller of the apparatus is entirely resident within said appliance plug or a plug-in module. See figure 1, col.4, lines 26-67.

Regarding claim 40, DePauli discloses wherein said apparatus enables an appliance electrically connected thereto to operate in a manner different from that originally intended. See figures 1-2.

Regarding claim 42, DePauli discloses a programmable controller(130) for providing an appliance plug or a plug-in module; disposing the programmable controller (20) within the appliance plug or plug-in module(4); providing a plurality of electrical power delivery conductors; programming the controller by applying one or more signals to two of the power delivery conductors. See figures 1, col.4, lines 1-67 to col.5, lines 1-67.

Regarding claim 43, DePauli discloses the programming step comprises applying one or more signals to no more than three of the power delivery conductors. See figure 1.

Regarding claim 44, DePauli discloses a plug (5) comprising programming the programmable controller(20) with electronic signals communicated from a programmer to the controller through one or more of the power delivery conductors after the controller has been disposed in the appliance plug or plug-in module. See figures 1, col.4, lines 1-67 to col.5, lines 1-67.

Regarding claim 45, DePauli discloses a high frequency signal to two of the power delivery conductors to place the programmable controller into a programming mode. See figures 1-2.

Regarding claim 46, DePauli discloses a series of pulses applied to two of the power delivery conductors to control both data and clock lines during programming. See figure 1, col.4, lines 26-67.

Regarding claim 47, DePauli discloses a mixture of direct current and alternating current signals to two of the power delivery conductors to place the programmable controller into a programming mode. See figures 1-2.

Regarding claim 48, DePauli discloses the programmable controller to implement a set of control actions. See figures 1-2.

Regarding claim 49, DePauli discloses a programmable controller comprises providing a microcontroller(20). See figure 1.

Regarding claim 51, DePauli discloses a controlling an appliance by programming the programmable controller so as to enable the appliance to perform in a manner different from its original design. See figures 1-2.

Regarding claim 52, figure 1, DePaudli discloses a circuit for saving power consumption in standby state comprising: a programmable controller (20); and electrostatic discharge protection diodes ((8) and fuse) internal to said programmable controller(20) and excluding (102) for rectifying a DC power supply external to said controller(20); wherein said internal electrostatic discharge protection diodes and Zener diode provide a source of direct current(DC) for said controller(20). See figures 1, col.4, lines 1-67 to col.5, lines 1-67.

Regarding claim 53, Depauli discloses each of said internal electrostatic discharge protection diodes (D and D1) are paralleled by transistor (6) that forms an alternative conducting path around said internal electrostatic discharge protection diode (8). See figure 1.

Regarding claim 54, DePaudli discloses the alternative conducting path allows

firing of a transistor during a portion of an AC cycle when said internal electrostatic discharge protection diodes are not conducting. See figures 1-2.

Regarding claim 55, DePaudli discloses a transistor that is in parallel with one of said internal electrostatic discharge protection diodes, wherein while applying a gate voltage to said MOSFET ensures that is turned on. See figures 1-2.

Claim Rejections - 35 USC § 103

6. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

7. Claims 37 and 50 are rejected under 35 U.S.C. 103(a) as being unpatentable over by DePauli (US 4, 963, 793) in view of Chang (US 5,477,279).

Regarding claims 37 and 50, DePaudli discloses the controller controls for protecting an appliance connected to a household.

DePaudli does not discloses the controller control selected from the group consisting of thyristors transistor , triac and combination.

Chang discloses the microprocessor having transistors (106-108). See figure 3.

It would have been an obvious to one of ordinary skill in the art at the time the invention was made to employ the transistors such as that suggested by Chang in the program control unit of DePaudli to turn on or off DC voltage from rectifying circuit and consume little static power for voltage and current.

Citation of relevant prior art

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Prior art Chapman et al. (U.S. Patent No. 6,150,940) discloses a an antitheft electrical power cord.

Prior art Mai et al (U.S. Patent No. 5,643,4012) discloses safety plug with switch means.

Inquiry

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Minh Dieu A whose telephone number is (571) 272-1817. The examiner can normally be reached on M-F (5:30 AM-2:45 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy Callahan can be reached on (571) 272-1740. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you

have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Examiner

Minh A

Art Unit 2821

10/10/06

shih-chao chen
SHIH-CHAO CHEN
PRIMARY EXAMINER